



# MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

Maisammguda, Dhulapally Post, Secunderabad 500100



## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

### TIME TABLE

03.07.2024

YEAR: II	SECTION: A	SEMESTER 1	AY: 2024-25	ROOM NO: 5102
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DAY	1 9:20AM- 10:20AM	2 10:20AM- 11:20AM	3 11:30AM- 12:30PM	12:30PM- 1:30PM	4 1:30PM- 2:30PM	5 2:30PM- 3:30PM
Monday	DLD	AC	PTSP	LUNCH	BS LAB	
Tuesday	SS	DLD LAB			PTSP	NAS
Wednesday	SS	AC LAB			DLD	AC
Thursday	NAS	SS	AC		FL	PTSP
Friday	PTSP	SS	NAS		DLD	AC
Saturday	AC	FL	DLD		NAS	PTSP

SUBJECT CODE	SUBJECT	NAME OF THE FACULTY
R22A0401	ANALOG CIRCUITS	Dr. R. CHINNA RAO
R22A0402	DIGITAL LOGIC DESIGN	E. MAHENDER REDDY
R22A0403	SIGNALS AND SYSTEMS	Dr. K. MALLIKARJUNA LINGAM
R22A0404	PROBABILITY THEORY AND STOCHASTIC PROCESSES	N. SARITHA
R22A0261	NETWORK ANALYSIS AND SYNTHESIS	Dr. K V RAMANA REDDY
R22A0481	ANALOG CIRCUITS LABORATORY	H/W (A1)
		H/W (A2)
		1. N.SARITHA 2. RENJU PANICKER
		1. K. VIJAYA BHARATHI 2. S REVATHI
R22A0482	DIGITAL LOGIC DESIGN LABORATORY	1.E.MAHENDER REDDY,2.V.SHIVARAJ KUMAR
R22A0483	BASIC SIMULATION LABORATORY	1.N. SARITHA, 2. R.RAMYA SRUMTHI
R22A0005	FRENCH LANGUAGE	K CHANDRASHEKAR
	CLASS TEACHER	E. MAHENDER REDDY
	YEAR COORDINATOR	Dr. R. CHINNA RAO

M.SREDHAR REDDY  
E.MAHENDER REDDY  
TT I/C

Dr. MALLIKARJUNA LK  
HOD

Dr. S. SRINIVASA RAO  
PRINCIPAL

Head of the Department  
Electronics & Communication Engineering  
Malla Reddy College of Engg. & Tech.  
(UGC-Autonomous Institution)  
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## DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



### TIME TABLE

03.07.2024

YEAR: II	SECTION: B	SEMESTER 1	AY: 2024-25	ROOM NO: 5107
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DAY	1 9:20AM- 10:20AM	2 10:20AM- 11:20AM	3 11:30AM 12:30PM	12:30PM -1:30PM	4 1:30PM- 2:30PM	5 2:30PM- 3:30PM
Monday	PTSP	DLD	SS	LUNCH	AC	NAS
Tuesday	AC	FL	NAS		DLD	SS
Wednesday	NAS	DLD LAB			DLD	PTSP
Thursday	PTSP	BS LAB			AC	SS
Friday	NAS	AC	SS		AC LAB	
Saturday	DLD	PTSP	AC		NAS	FL

SUBJECT CODE	SUBJECT	NAME OF THE FACULTY	
R22A0401	ANALOG CIRCUITS	Dr. R. CHINNA RAO	
R22A0402	DIGITAL LOGIC DESIGN	K.SURESH	
R22A0403	SIGNALS AND SYSTEMS	V.KIRAN KUMAR	
R22A0404	PROBABILITY THEORY AND STOCHASTIC PROCESSES	N. SARITHA	
R22A0261	NETWORK ANALYSIS AND SYNTHESIS	R.SAI KRISHNA	
R22A0481	ANALOG CIRCUITS LABORATORY	H/W (B1)	H/W (B2)
		1.NEHA THAKUR 2. A.DILEEP	1. K. VIJAYA BHARATH 2. D.SANTHOSH
R22A0482	DIGITAL LOGIC DESIGN LABORATORY	1.K.SURESH, 2.NEHA THAKUR	
R22A0483	BASIC SIMULATION LABORATORY	1.V. SHIVA RAJ KUMAR, 2.S.DEEPIKA	
R22A0005	FRENCH LANGUAGE	K CHANDRASHEKAR	
	CLASS TEACHER	N SARITHA	
	YEAR COORDINATOR	Dr. R. CHINNA RAO	

M.SREDHAR REDDY *MRS*  
E.MAHENDER REDDY *MAH*  
TT I/C

*[Signature]*  
Dr. MALLIKARJUNA LK  
Head of Department  
Electronics & Communication Engineering  
Malla Reddy College of Engg. & Tech.  
(UGC-Autonomous Institution)  
Secunderabad-500100

*[Signature]*  
Dr. S. SRINIVASA RAO  
PRINCIPAL  
PRINCIPAL  
Malla Reddy College of Engg. & Technology  
UGC AUTONOMOUS INSTITUTION  
Maisammguda, Dhulapally, Secunderabad-500100